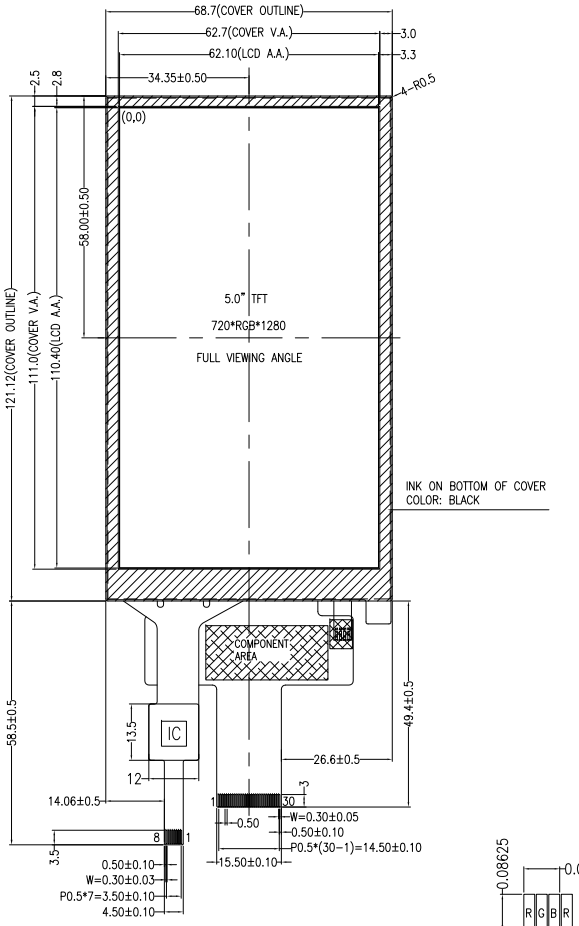
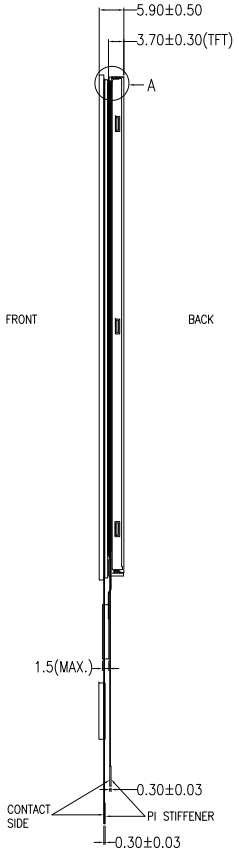
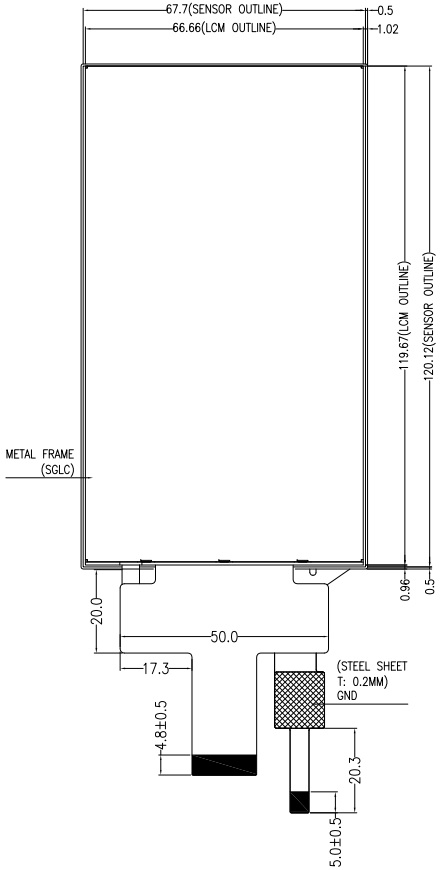
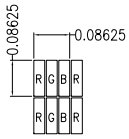




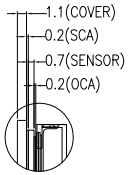


PIN ASSIGNMENT FOR LCM	
PIN	SYMBOL
1	GND
2	GND
3	LRSTB
4	LPTE
5	VCI
6	VCI
7	LCD_ID
8	MIPI_TDN3
9	MIPI_TDP3
10	GND
11	MIPI_TDN2
12	MIPI_TDP2
13	GND
14	LEDA
15	LEDA
16	LEDK
17	LEDK
18	GND
19	MIPI_TCN
20	MIPI_TCP
21	GND
22	MIPI_TDN0
23	MIPI_TDP0
24	GND
25	MIPI_TDN1
26	MIPI_TDP1
27	GND
28	NC
29	NC
30	NC

PIN ASSIGNMENT FOR CTP	
PIN	SYMBOL
1	VSS
2	NC
3	VDD
4	SCL
5	SDA
6	INT
7	RST
8	VSS



PIXEL DETAIL
(SCALE 100:1)



DETAIL A
(SCALE 2:1)

BACKLIGHT CIRCUIT DIAGRAM

NOTES FOR TFT:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCI=2.8V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: ILI9881C
5. LED BACKLIGHT: WHITE LED, If=60mA, Vf=19.6V ~ 23.8V
6. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
7. RECOMMEND MATCHING CONNECTOR: HRS FH19SC-30S-0.5SH OR EQUIVALENT

NOTES FOR CTP:

1. 5.0 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCA
3. OPERATING VOLTAGE: VDD=3.3V
4. DRIVER IC: GT967
5. INTERFACE: IIC
6. RESOLUTION: 720*1280
7. MULTI-FINGER: UP TO 5
8. HARDNESS OF COVER SURFACE: ≥6H
9. FIRMWARE FILE: TBD

NOTES FOR OTHERS:

1. OPERATING TEMP.: -20°C ~ 70°C
2. STORAGE TEMP.: -30°C ~ 80°C
3. SURFACE LUMINANCE: 340 CD/M^2 (TYP.)
4. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
5. GENERAL TOLERANCE: ±0.3MM
6. ROHS COMPLIANT

MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA 2024.03.04			MODULE P/N:	
CHECKED BY:			SCALE : 1/1	MI0500APT-1CP	
APPROVED BY:			UNIT: mm	DESCRIPTION:	
VERSION NO:	01		SHEET NO.: 1/1	TFT + CTP MODULE	

VER.	REVISED DESCRIPTION	REVISER	DATE
01	FIRST ISSUE	RITA	2024.03.04